



EXPRESS MAIL NO. EV336594936US

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Francesco Pappalardo et al.
Application No. : 10/682,378
Filed : October 9, 2003
For : RECONFIGURABLE CONTROL STRUCTURE FOR CPUS AND
METHOD OF OPERATING SAME

Docket No. : 851763.443
Date : March 8, 2004

Mail Stop Missing Parts
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents:

In accordance with 37 C.F.R. §§ 1.56 and 1.97 through 1.98, applicants wish to make known to the U.S. Patent and Trademark Office the references set forth on the attached form PTO-1449. Applicants note that since the application filing date is after June 30, 2003, copies of the cited U.S. patents and published patent applications are not required and accordingly have not been provided. Copies of all other cited references are enclosed. As to any reference cited, applicants do not admit that it is "prior art" under 35 U.S.C. §§ 102 or 103, and specifically reserve the right to traverse or antedate any such reference, as by a showing under 37 C.F.R. § 1.131 or other method. Although the aforesaid references are made known to the Patent and Trademark Office in compliance with applicants' duty to disclose all information they are aware of which is believed relevant to the examination of the above-identified application, applicants believe that their invention is patentable.

Please acknowledge receipt of this Information Disclosure Statement and kindly make the cited references of record in the above-identified application.

Applicants believe this Information Disclosure Statement has been timely filed, however, the Director is authorized to charge any fee due by way of this Information Disclosure Statement to our Deposit Account No. 19-1090.

Respectfully submitted,

Seed Intellectual Property Law Group PLLC



Robert Iannucci

Registration No. 33,514

Enclosures:

Form PTO-1449

Cited References (5)

701 Fifth Avenue, Suite 6300
Seattle, Washington 98104-7092
Phone: (206) 622-4900
Fax: (206) 682-6031

439991



EXPRESS MAIL NO. EV336594936US

Sheet 1 of 1

FORM PTO-1449
(REV. 7-80)U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.
851763.443APPLICATION NO.
10/682,378INFORMATION DISCLOSURE STATEMENT
(Use several sheets if necessary)

APPLICANTS

Francesco Pappalardo et al.

FILING DATE

October 9, 2003

GROUP ART UNIT

2183

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA	6,049,862	04/11/00	Bauer et al.	712	208	
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION	
					YES	NO
	AK					
	AL					
	AM					

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

✓		Ertem, M., "A Reconfigurable Co-Processor for Microprocessor Systems," in <i>Proceedings of the Southeast Conference</i> , Tampa, April 5-8, 1987, New York, IEEE, vol. 1, pp. 225-228.
✓		"Design Flexibility into Hardwired Logic," <i>IBM Technical Disclosure Bulletin</i> 37(3):321-324, March 1994.
/	AP	Hennessy, J. et al., <i>Computer Organization and Design. The Hardware/Software Interface</i> , Morgan Kaufman Publishers, San Francisco, 1994, Appendix C, "Mapping Control to Hardware", pp. C3-C28.
/	AQ	Jain, R., "An Alternative Approach Towards the Design of Control Units," <i>Microelectronics and Reliability</i> 24(6):1009-1012, 1984.
/	AR	"Opcode Compare Facility," Research Disclosure No. 315, Kenneth Mason Publications, p. 577, July 1990.

EXAMINER

DATE CONSIDERED

* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).